

Docket No.: 67161-131

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Satoshi SHIMIZU	:	Confirmation Number:
	:	
Serial No.:	:	Group Art Unit:
	:	
Filed: November 20, 2003	:	Examiner: Unknown
	:	
For: SEMICONDUCTOR DEVICE WITH DUMMY ELECTRODE	:	

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

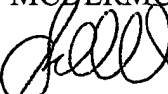
The relevance of Japanese Patent Laying-Open No. 2002-26156 is discussed in the present specification.

**Serial No.:**

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



Stephen A. Becker

Registration No. 26,527

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202) 756-8000 SAB:tlb  
Facsimile: (202) 756-8087  
**Date: November 20, 2003**

SERIAL NO.

FILING DATE  
**November 20, 2003**

GROUP

(PTO-1449)

## U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP P2002-26156A	01/25/2002	MITSUBISHI ELECTRIC CORP		(Japan w/English Abstract)	
		JP P2000-114481A	04/21/2000	NEC CORP		(Japan w/English Abstract)	
		JP P2000-77535A	03/14/2000	HITACHI LTD		(Japan w/English Abstract)	

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		"NAND Flash Memory", F. Masuoka, Flash Memory Business Symposium, June 16, 1993, pp. 1-11
		"DINOR Flash Memory", Y. Terada, Flash Memory Business Symposium, June 16, 1993, pp. 1-7
		"NOR Flash Memory", T. Okasawa, Flash Memory Business Symposium, June 16, 1993, pp. 1-6

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.